



#3
7-10-2 2122

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Spiros Kalogeropoulos

Assignee: Sun Microsystems, Inc.

Title: ENHANCED INSTRUCTION SCHEDULING AFTER REGISTER
ALLOCATION BY EMPLOYING TRACES

Serial No.: 10/066,061

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Examiner: Unknown

Group Art 2185
Unit:

Docket No.: P-7286

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Monterey, CA
April 16, 2002

ASSISTANT COMMISSIONER FOR PATENTS
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SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicant submits eight (8) sheets of formal drawings consisting of Figures 1, 2, 3, 4A, 4B, 5, 6, 7, 8, 9, 10, 11 and 12. If there are any questions concerning the formal drawings, please call the undersigned at the number given.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on April 16, 2002

Attorney for Applicant(s)

April 16, 2002

Date of Signature

Respectfully submitted,

Serge J. Hodgson
Attorney for Applicant(s)
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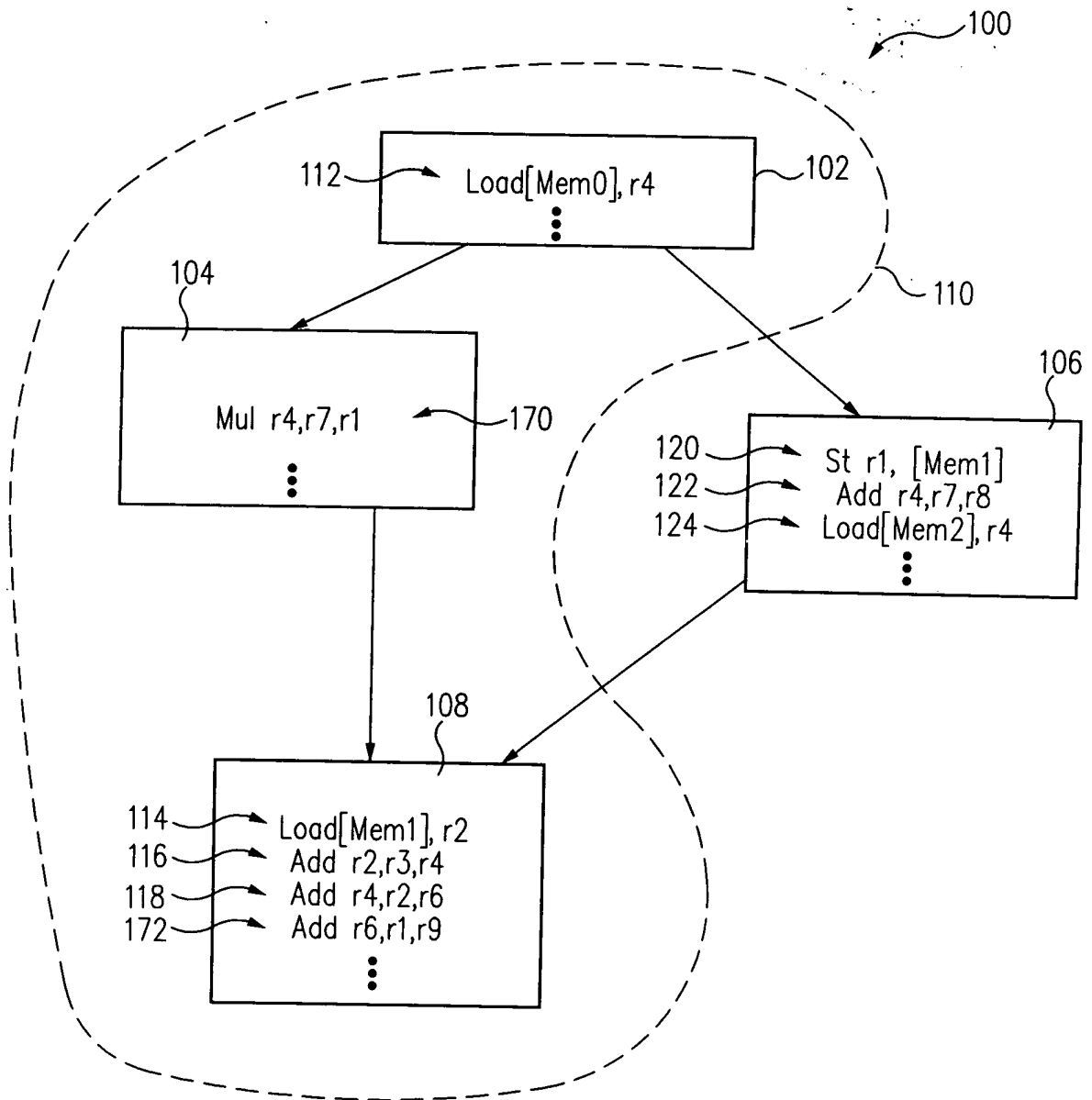


FIG. 1

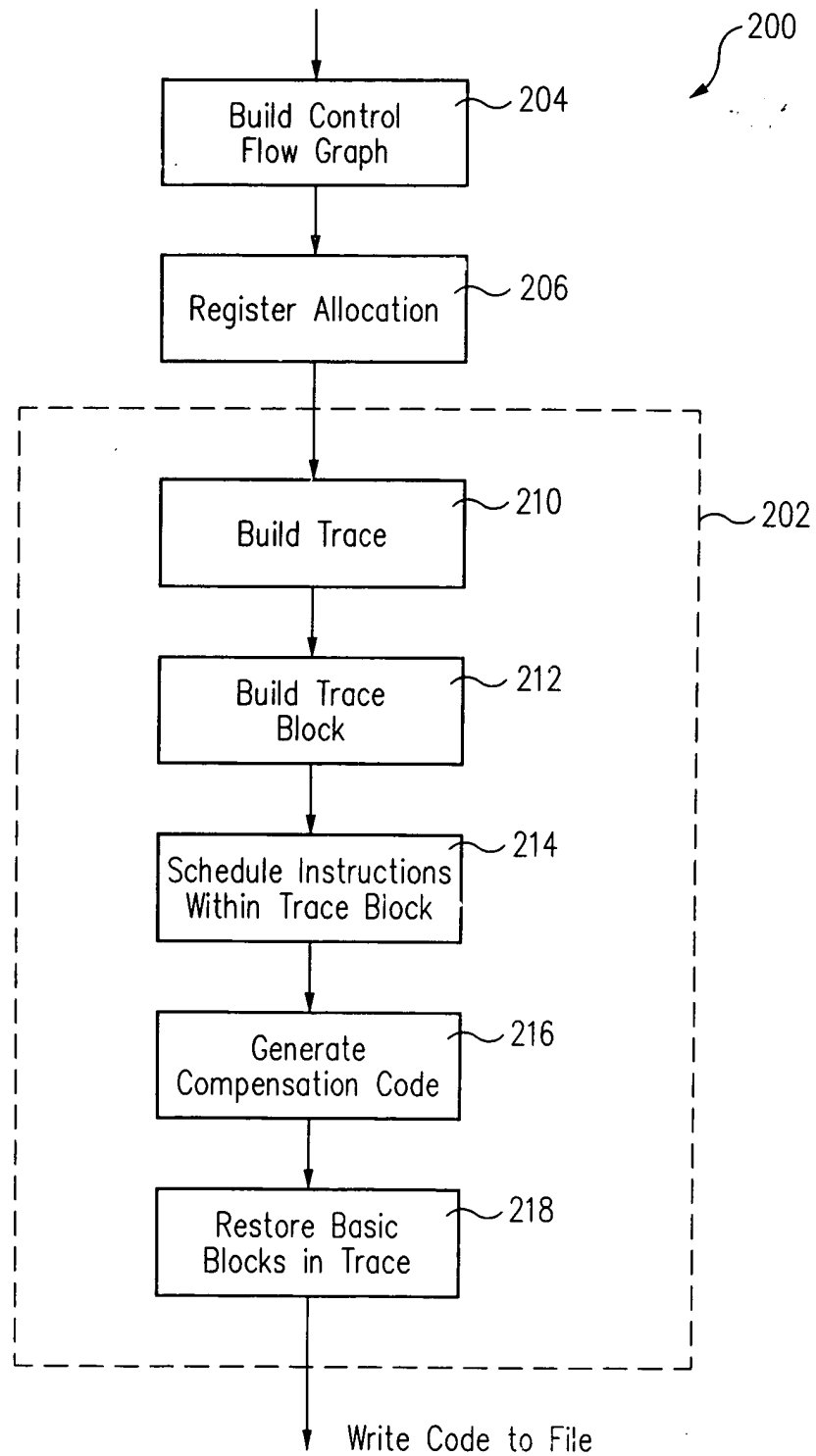
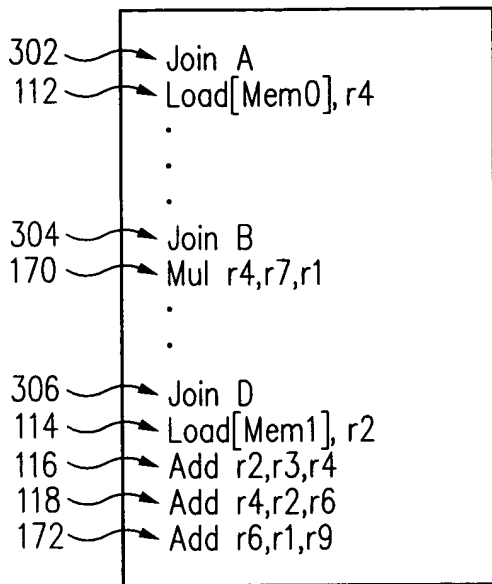
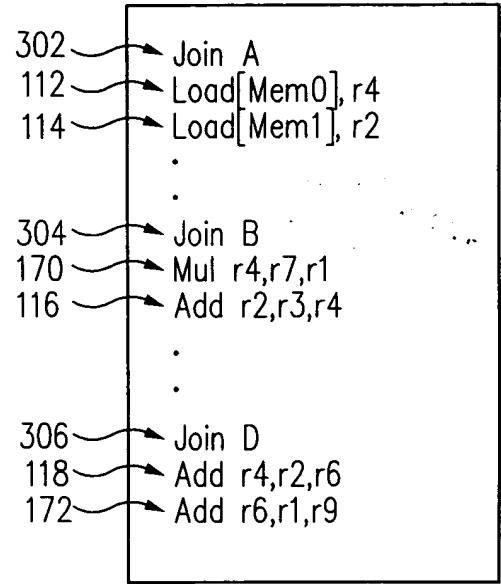


FIG. 2



300 FIG. 3



300 FIG. 6

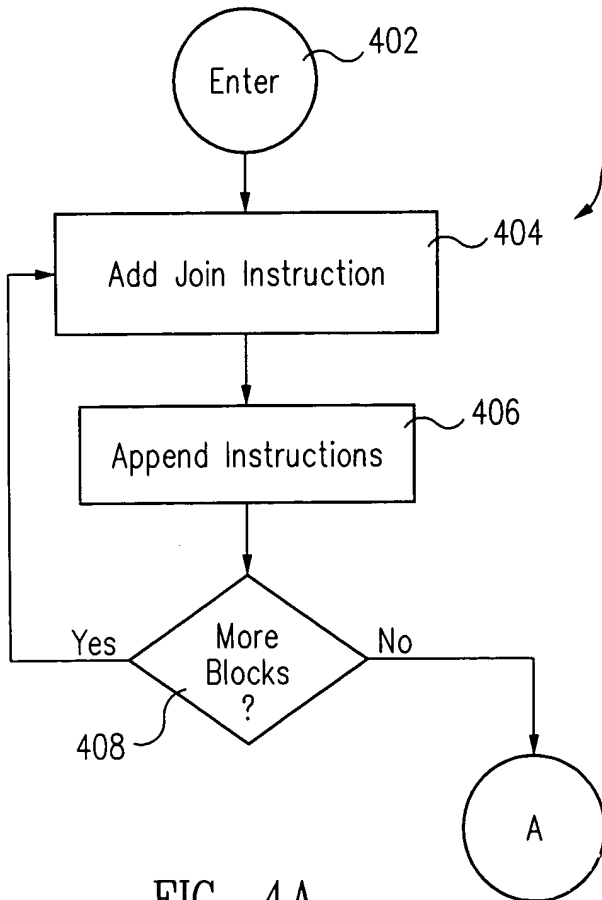


FIG. 4A

212

Join B → Used {r1,r4,r7}

FIG. 5

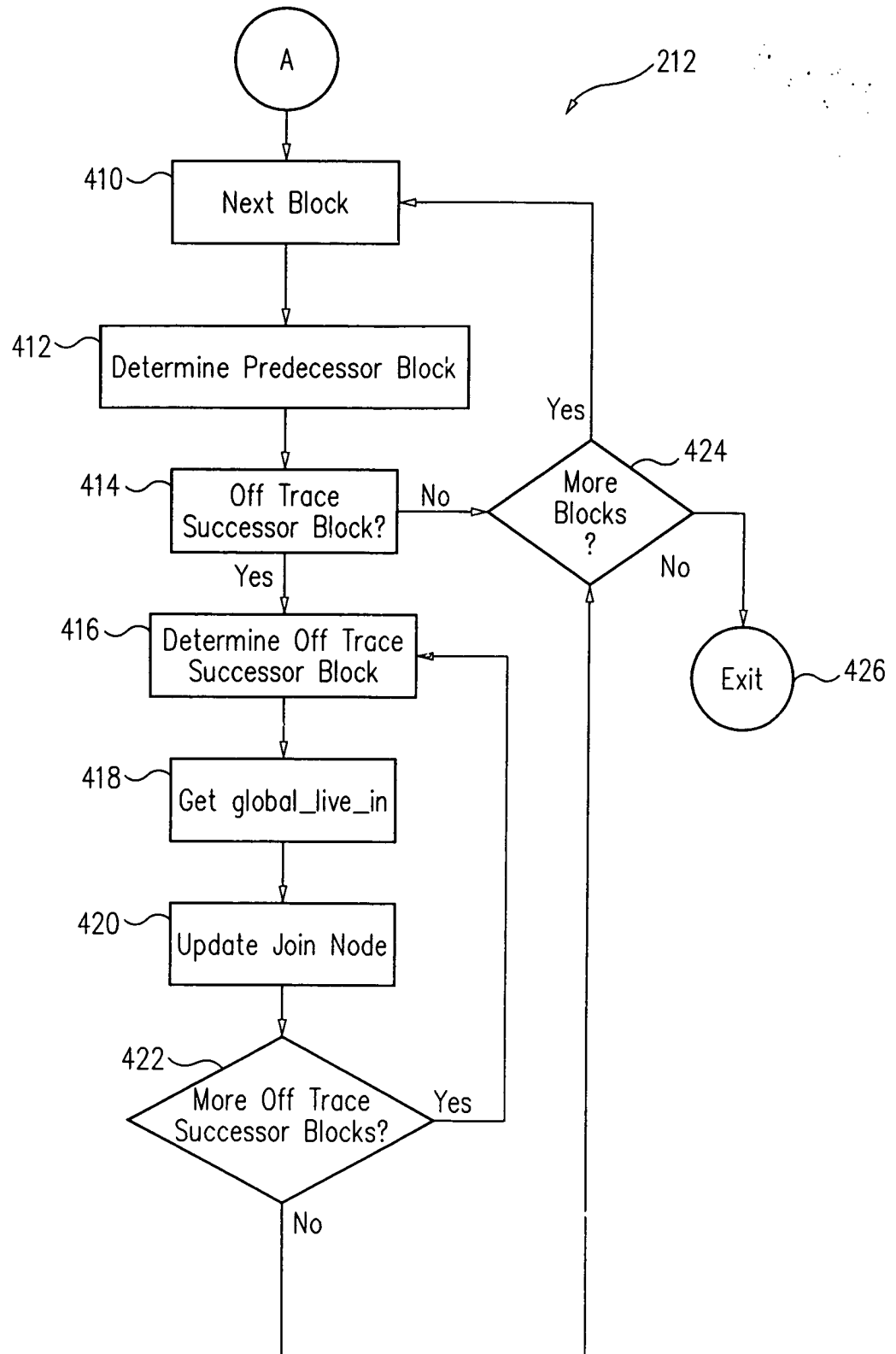


FIG. 1R

FIG. 7

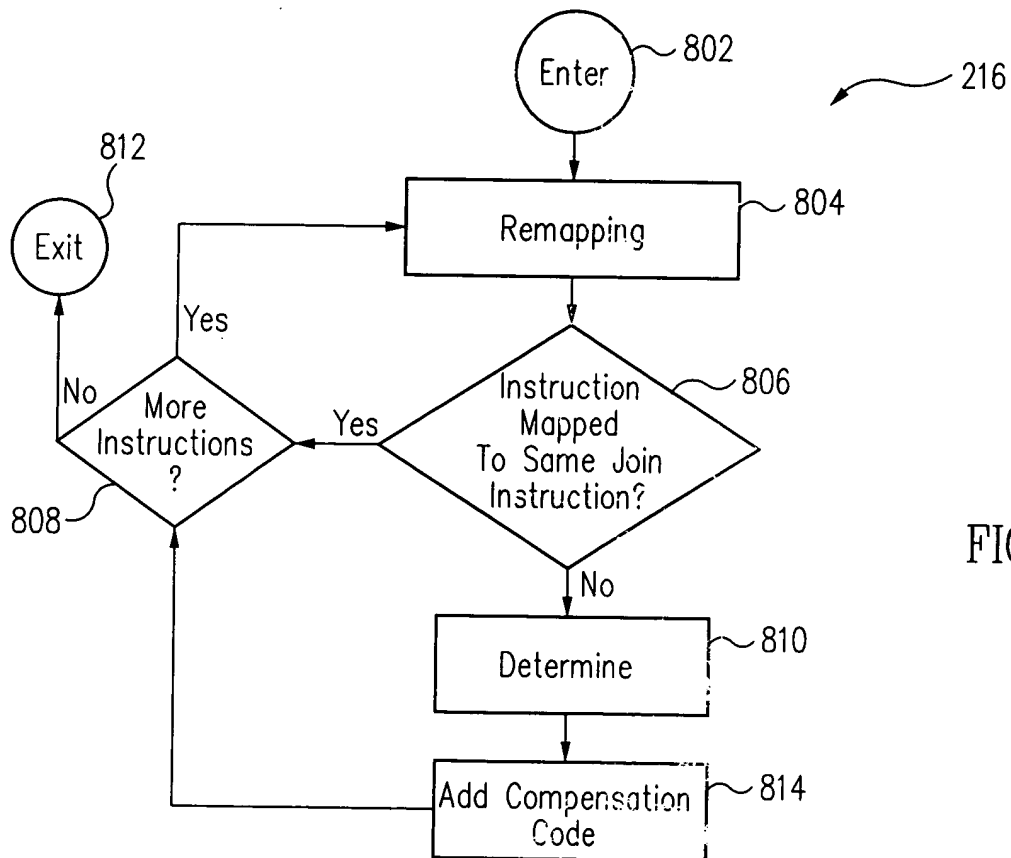
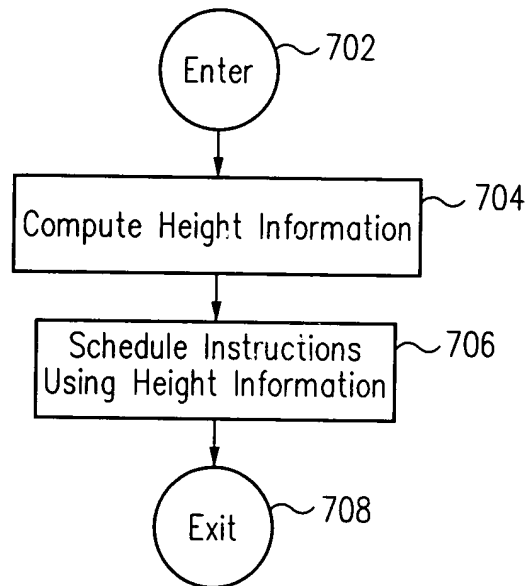


FIG. 8

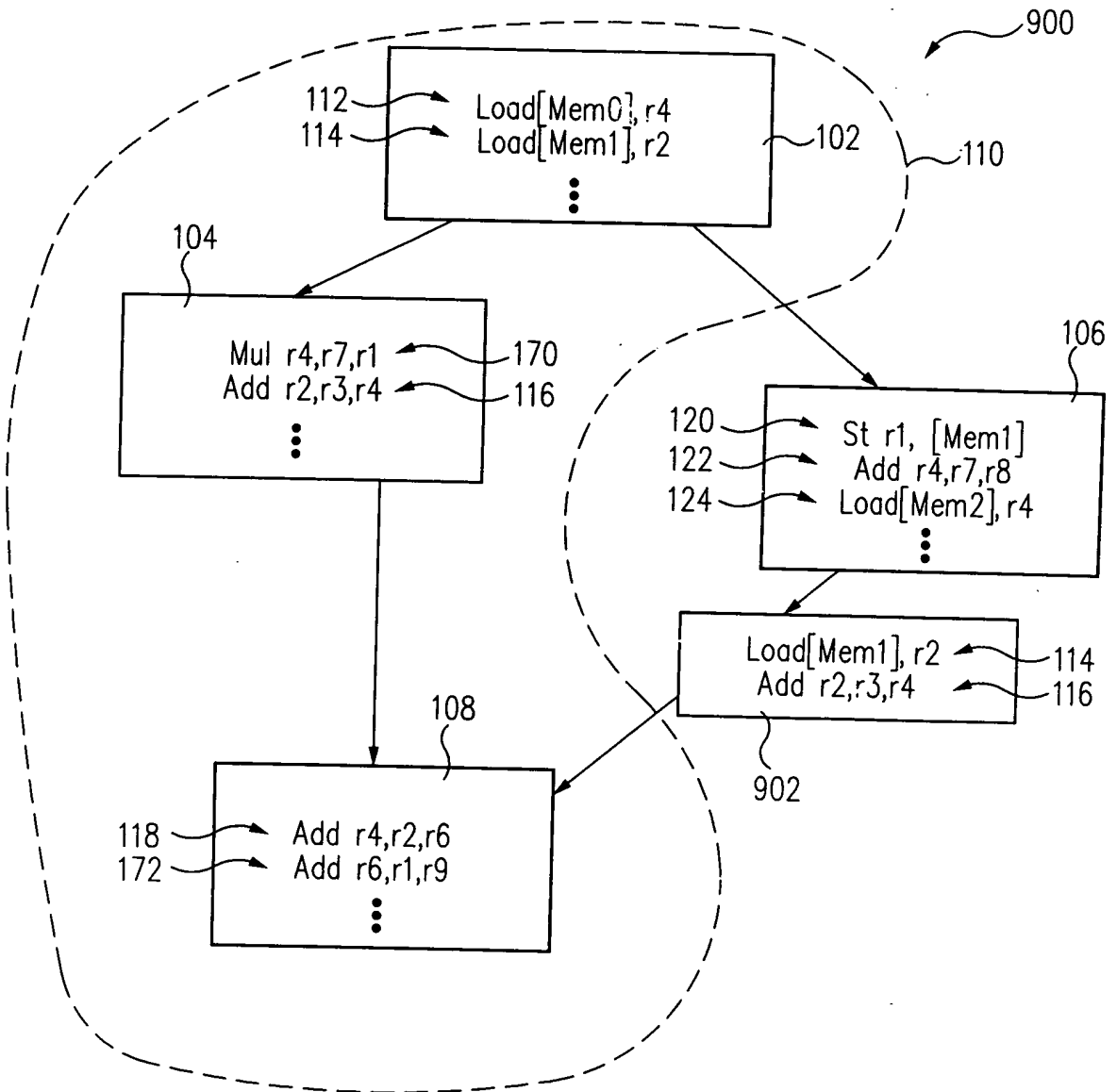


FIG. 9

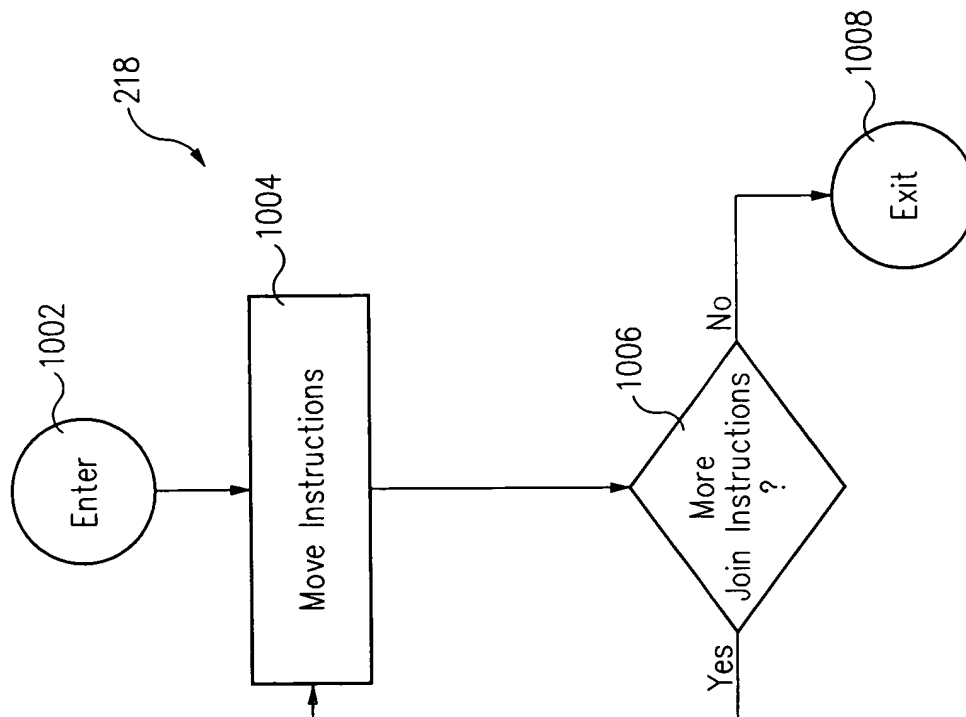


FIG. 10

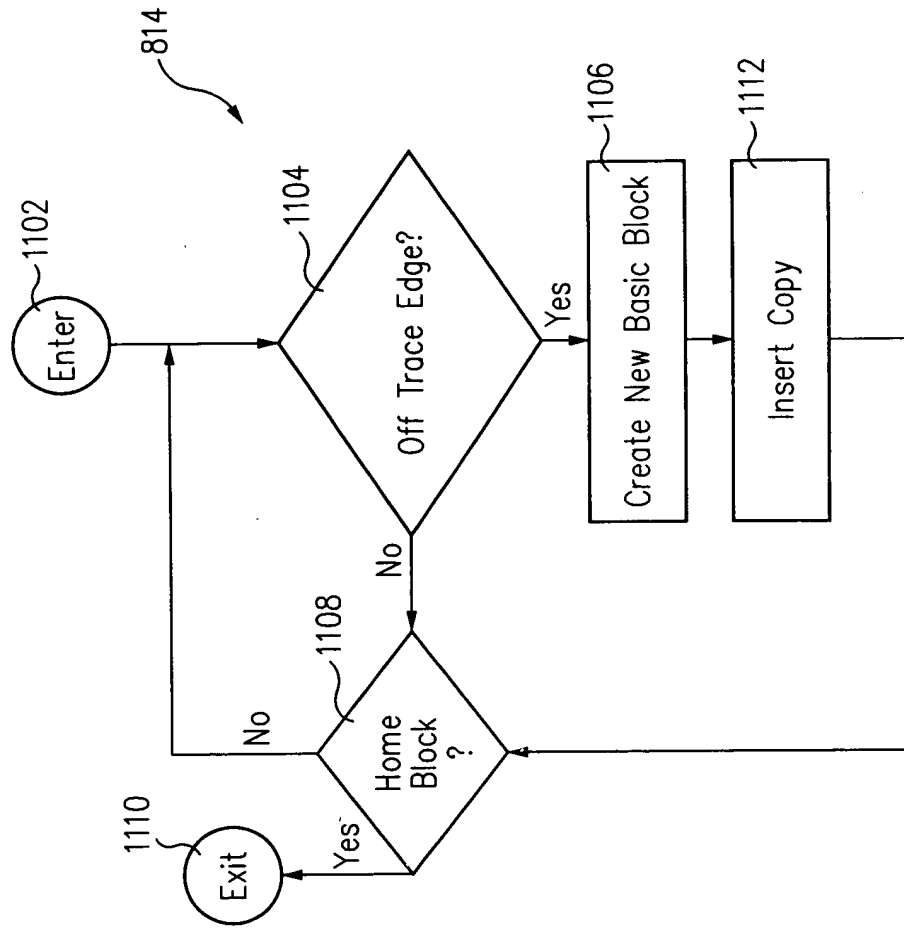


FIG. 11

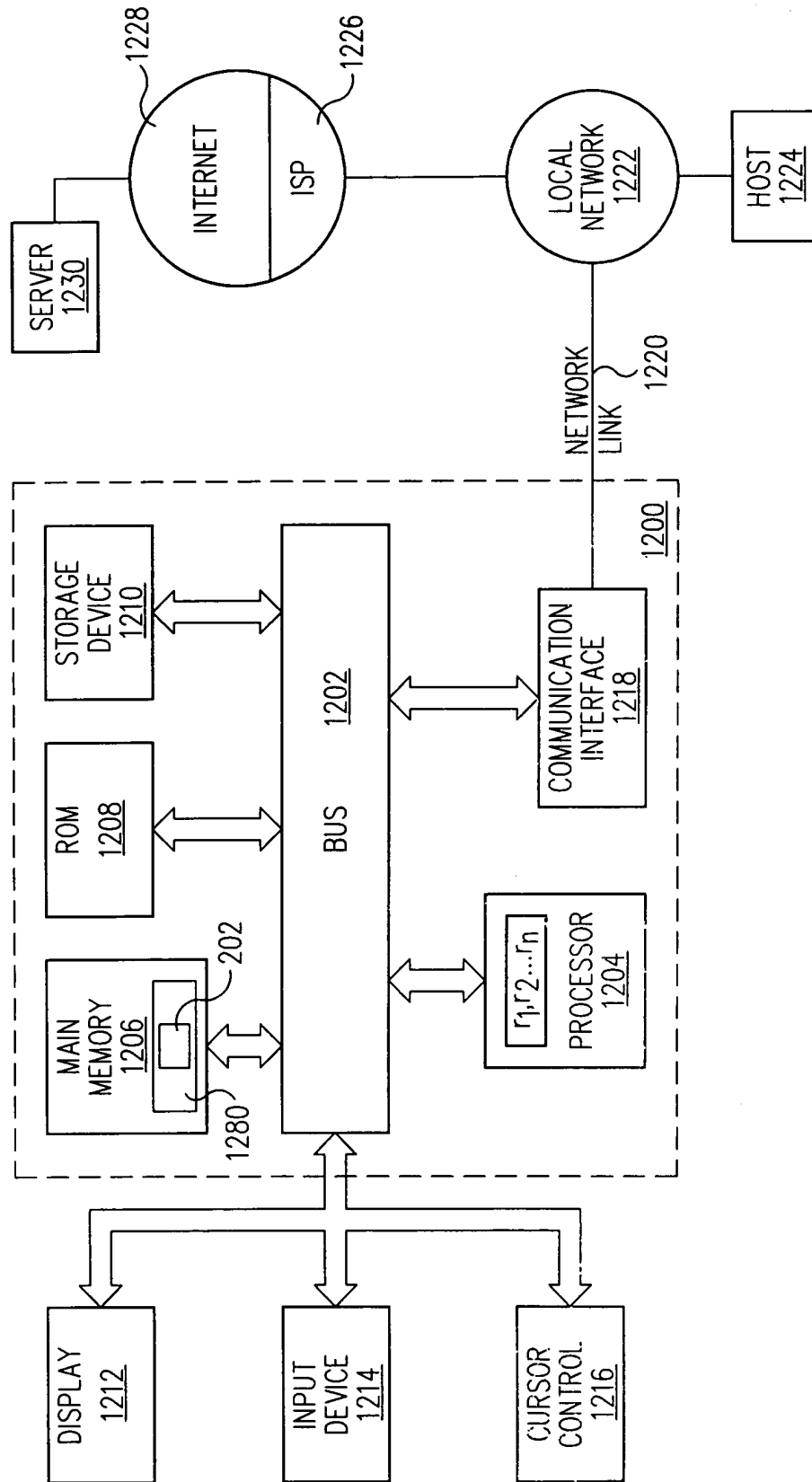


FIG. 12